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PATENT

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Applicant: Pechanek, et al.

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For: METHODS AND APPARATUS FOR ABBREVIATED INSTRUCTION AND
CONFIGURABLE PROCESSOR ARCHITECTURE

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Examiner: K. Kim

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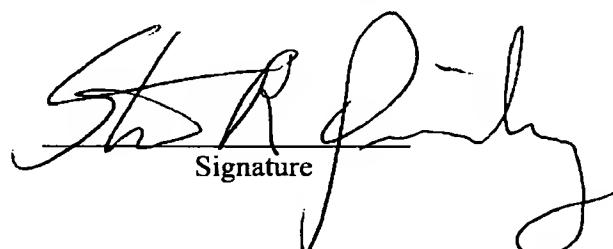
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